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48134 7390 09/22/2005

SLATER & MATSIL LLP
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Kristin R. Hayes (Depositor's name)
K. Hayes (Signature)
December 12, 2005 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/753,407	01/09/2004	Michael Bernhard Sommer	INF-126	6375

TITLE OF INVENTION: INTEGRATED SEMICONDUCTOR MEMORY CIRCUIT AND METHOD OF MANUFACTURING THE SAME

APPL. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	12/22/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
SOWARD, IDA M	2822	257-371000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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- (1) the names of up to 3 registered patent attorneys or agents OR, alternatively,
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3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(A) NAME OF ASSIGNEE

Infineon Technologies AG

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Munich, Germany

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(e)(2).

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Authorized Signature

Ira S. Matsil

Date December 12, 2005

Typed or printed name

Ira S. Matsil

Registration No. 35,272

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FACSIMILE COVER SHEET

To: Commissioner for Patents	Total Pages Sent: 3
Mail Stop Issue Fee	(including cover sheet)
Facsimile Number: (571) 273-2885	Transmission Date: December 12, 2005

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Sommer	Docket No.:	INF-126
Serial No:	10/753,407	Art Unit:	2822
Date Filed:	January 9, 2004		
Title:	Integrated Semiconductor Memory Circuit and Method of Manufacturing the Same		

CERTIFICATION OF FACSIMILE TRANSMISSION

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- Certification of Facsimile Transmission (1 page)
- Part B - Fee Transmittal (2 duplicate pages)

Respectfully submitted,

Kristin R. Hayes
Legal Assistant

Confirmation Respectfully Requested

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